



WORK EXPERIENCE

Synopsys Inc.

Analog Design Engineer

Senior	2022-present
Mid	2020-2022
Junior	2018-2020
Intern	2018 ( Mar - Jul )

National Polytechnic University of Armenia Lecturer, 2020 (present)

- IC Design Introduction



EDUCATION

NPUA/SAED, Microelectronic Engineering, VLCD Design Doctor of Philosophy -PhD (2020-2022)

NPUA/SAED, Microelectronics and Nanoelectronics Master’s degree (2018-2020)

NPUA/SAED, Microelectronics and Nanoelectronics Bachelor’s degree (2014-2018)

QUALIFICATIONS & SKILLS

- Knowledge of analog/digital integrated circuit design fundamentals
- Synopsys EDA tools/IC design flows
- Variability analysis and debug of integrated circuits
- Knowledgeable about CMOS fabrication processes
- Experience of work with advance technological nodes: 3/5/7nm, 22nm FD-SOI
- EMIR drop analysis on post layout design

LANGUAGE

- Armenian (Native or Bilingual Proficiency)
- Russian (Native or Bilingual Proficiency)
- English (Full Professional Proficiency)